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INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Not for submission under 37 CFR 1.99)</i>	Application Number	10882468
	Filing Date	2004-04-12
	First Named Inventor	Mitchell Alsup
	Art Unit	2183
	Examiner Name	George D. Zalepa
	Attorney Docket Number	5500-92000

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	4	6449714		2002-09-10	Sinharoy	
	5	6339822		2002-01-15	Miller	
	6	6256727		2001-07-03	McDonald	
	7	6185675		2001-02-06	Kranich et al	
	8	6823428		2004-11-23	Rodriguez et al	

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9	6167536		2000-12-26	Moann	
10	6357016		2002-03-12	Rodgers et al	
11	7003629		2006-02-21	Alsup	
12	6345295		2002-02-05	Beardsley et al	
13	5930497		1999-07-17	Cherian et al	
14	6578128		2003-06-10	Arsenault et al	
15	6973543		2005-12-06	Hughes	
16	6216206		2001-04-10	Peled et al	
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	4	20040083352		2004-04-29	Lee	
	5	20040193857		2006-11-16	Miller et al	
	6	20050125632		2005-06-09	Alsup et al	
	7	20020095553		2002-04-18	Mendelson et al	
	8	20050076180		2005-04-07	Smaus et al	
	9	20050125613		2005-06-09	Kim et al	
	10	20040216091		2004-10-28	Groeschel	

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	1	Jacobson, et al., "Instruction Pre-Processing in Trace Processors," IEEE Xplore, January 1999, 6 pages.	<input type="checkbox"/>
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	6	Hinton, G., et al., "A 0.18-MUM CMOS IA-32 Processor with a 4-GHZ Integer Execution Unit," IEEE Journal of Solid-State Circuits, Vol. 36, No. 11, November 2001, pages. 1617-1627.	<input type="checkbox"/>
	7	Palmer, et al., "Fido: A Cache That Learns to Fetch," Proceedings of the 17th International Conference on Very Large Data Bases, Barcelona, September 1991, pp. 255-264.	<input type="checkbox"/>
	8	Chen et al., "Eviction Based Cache Placement for Storage Caches," USENIX 2003 Annual Technical Conference, (13 pages).	<input type="checkbox"/>
	9	Merten, et al., "An Architectural Framework for Run-Time Optimization," June 2001, pp. 1-43.	<input type="checkbox"/>
	10	Jourdan, et al., "Increasing the Instruction-Level Parallelism through Data-Flow Manipulation," Intel, 11 pages.	<input type="checkbox"/>
	11	Bryan Black, et al., "Turboscalar: A High Frequency High IPC Microarchitecture," Dept. of Electrical and Computer Engineering, Carnegie Mellon Univ., June 2000.	<input type="checkbox"/>
	12	Grant Braught, "Clas #21-Assemblers, Labels & Pseudo Instructions," Dickenson College, Fall Semester 2000, 6 pages.	<input type="checkbox"/>
	13	U.S. Application Serial No. 10/676,437 filed 10/1/03, Alsup, et al.	<input type="checkbox"/>
	14	U.S. Application Serial No. 10/615,506 filed 7/8/03, Alsup	<input type="checkbox"/>
	15	U.S. Application Serial No. 10/755,742 filed 1/12/04, Smaus, et al.	<input type="checkbox"/>

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See attached certification statement.
 Fee set forth in 37 CFR 1.17 (p) has been submitted herewith.
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A signature of the applicant or representative is required in accordance with CFR 1.33, 10.18. Please see CFR 1.4(d) for the form of the signature.

Signature	/Robert C. Kowert/	Date (YYYY-MM-DD)	2006-11-16
Name/Print	Robert C. Kowert	Registration Number	39,255

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